N-Channel 30-V (D-S) MOSFET

CHARACTERISTICS

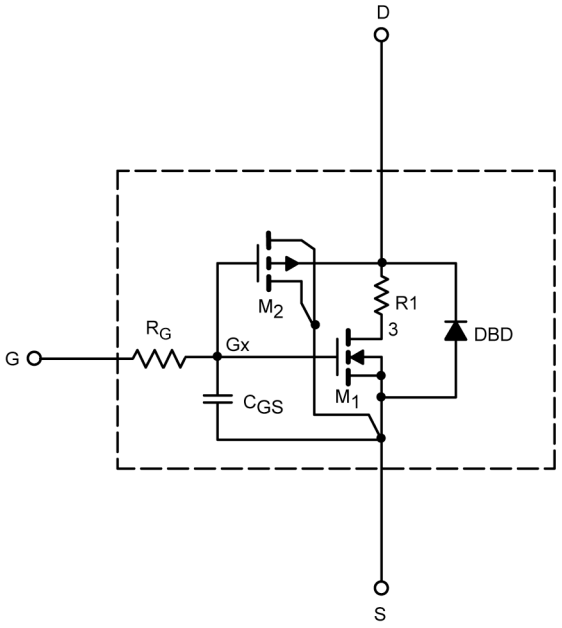
- N-Channel Vertical DMOS
- Macro Model (Subcircuit Model)
- Level 3 MOS
- Apply for both Linear and Switching Application
- Accurate over the - 55 °C to 125 °C Temperature Range
- Model the Gate Charge, Transient, and Diode Reverse Recovery Characteristics

DESCRIPTION

The attached spice model describes the typical electrical characteristics of the N-channel vertical DMOS. The subcircuit model is extracted and optimized over the - 55 °C to 125 °C temperature ranges under the pulsed 0 V to 10 V gate drive. The saturated output impedance is best fit at the gate bias near the threshold voltage.

A novel gate-to-drain feedback capacitance network is used to model the gate charge characteristics while avoiding convergence difficulties of the switched C_{gd} model. All model parameter values are optimized to provide a best fit to the measured electrical data and are not intended as an exact physical interpretation of the device.

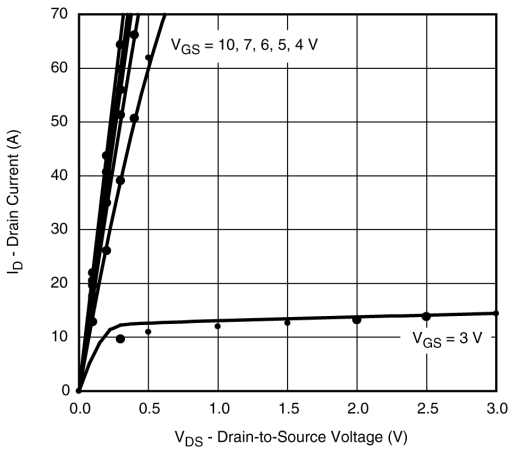
SUBCIRCUIT MODEL SCHEMATIC

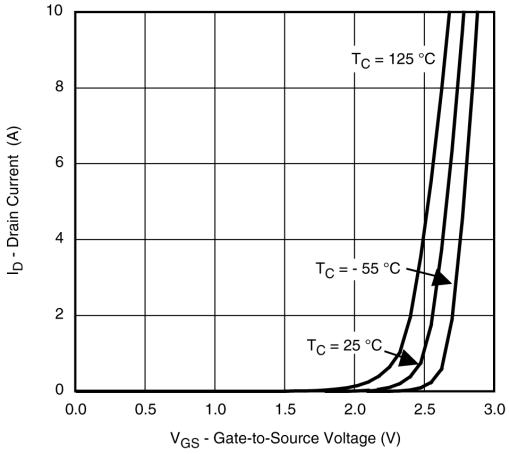


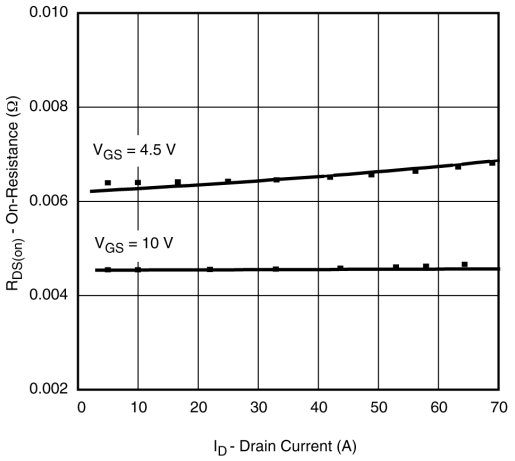
SPECIFICATIONS (T_J = 25 °C UNLESS OTHERWISE NOTED)					
Parameter	**Symbol**	**Test Condition**	**Simulated Data**	**Measured Data**	**Unit**
Static					
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250 μA	1.7		V
Drain-Source On-State Resistance[a]	$R_{DS(on)}$	V_{GS} = 10 V, I_D = 20 A	0.0045	0.0048	Ω
		V_{GS} = 4.5 V, I_D = 17.5 A	0.0063	0.0064	
Forward Transconductance[a]	g_{fs}	V_{DS} = 15 V, I_D = 20 A	60	82	S
Diode Forward Voltage[a]	V_{SD}	I_S = 10 A	0.79	0.80	V
Dynamic[b]					
Input Capacitance	C_{iss}	V_{DS} = 15 V, V_{GS} = 0 V, f = 1 MHz	1640	1700	pF
Output Capacitance	C_{oss}		373	350	
Reverse Transfer Capacitance	C_{rss}		121	140	
Total Gate Charge	Q_g	V_{DS} = 15 V, V_{GS} = 10 V, I_D = 20 A	25	28	nC
		V_{DS} = 15 V, V_{GS} = 4.5 V, I_D = 20 A	13	14	
Gate-Source Charge	Q_{gs}		5.4	5.4	
Gate-Drain Charge	Q_{gd}		4.6	4.6	

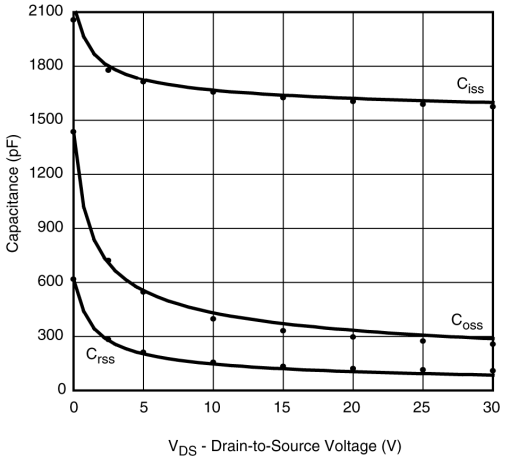
Notes
a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %.
b. Guaranteed by design, not subject to production testing.

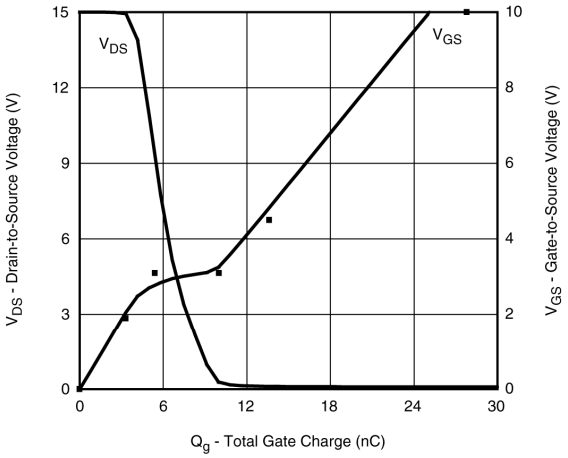
COMPARISON OF MODEL WITH MEASURED DATA (T_J = 25 °C UNLESS OTHERWISE NOTED)

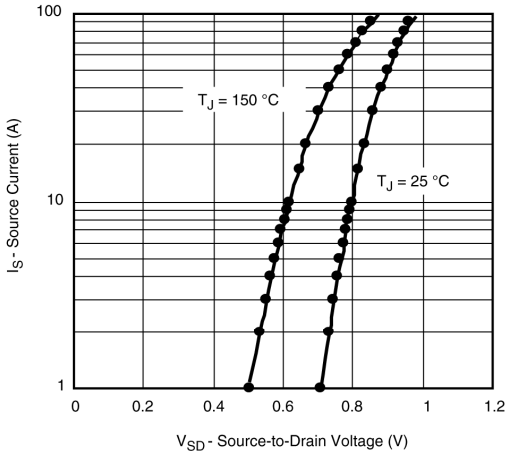












Note: Dots and squares represent measured data.

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